Ref	Num	Sch Name	BGA	Type	Description	Pin Mux									
						Mode 0	Mode 1	Mode 2	Mode 3	Mode 4	Mode 5	Mode 6	Mode 7	Mode 8	Mode 9
	1	GND													
	2	ETH1_LED2			PHY Link LED										
	3	ETH1_LED1			PHY Status LED										
	4	ETH1_RD1+			Ethernet Data 1 Positive										
	5	ETH1_TD1+			Ethernet Data 0 Positive										
	6	ETH1_RD1-			Ethernet Data 1 Negative										
	7	ETH1_TD1-			Ethernet Data 0 Negative										
	8	ETH1_RD2+			Ethernet Data 3 Positive										
	9	ETH1_TD2+			Ethernet Data 2 Positive										
	10	ETH1_RD2-			Ethernet Data 3 Negative										
	11	ETH1_TD2-			Ethernet Data 2 Negative										
	12	GND													
	13	GND													
	14	UART0_RXD	K25	I	UART0 Receive Data	uart0_rxd	spi1_cs0	dcan0_tx	I2C2_SDA	eCAP2_in_PWM2_out	pr0_pru1_gpo4	pr0_pru1_gpi4	gpio1_10		
	15	UART0_RTSN	J25	O	UART0 Request to Send	uart0_rtsn	uart4_txd	dcan1_rx	I2C1_SCL	spi1_d1	spi1_cs0	pr1_edc_sync1_out	gpio1_9		
	16	UART0_TXD	J24	O	UART0 Transmit Data	uart0_txd	spi1_cs1	dcan0_rx	I2C2_SCL	eCAP1_in_PWM1_out	pr0_pru1_gpo5	pr0_pru1_gpi5	gpio1_11		
	17	UART0_CTSN	L25	I	UART0 Clear to Send	uart0_ctsn	uart4_rxd	dcan1_tx	I2C1_SDA	spi1_d0	timer7	pr1_edc_sync0_out	gpio1_8		
	18	DCAN1_RX	L21	IOD	DCAN1 Receive Data	uart1_txd	mmc2_sdwp	dcan1_rx	I2C1_SCL		pr1_uart0_txd	pr1_pru0_gpi16	gpio0_15		
	19	DCAN0_RX	L22	IOD	DCAN0 Receive Data	uart1_rtsn	timer5	dcan0_rx	I2C2_SCL	spi1_cs1	pr1_uart0_rts_n	pr1_edc_latch1_in	gpio0_13		
	20	DCAN1_TX	K21	IOD	DCAN1 Transmit Data	uart1_rxd	mmc1_sdwp	dcan1_tx	I2C1_SDA		pr1_uart0_rxd	pr1_pru0_gpi16	gpio0_14		
	21	DCAN0_TX	K22	IOD	DCAN0 Transmit Data	uart1_ctsn	timer6	dcan0_tx	I2C2_SDA	spi1_cs0	pr1_uart0_cts_n	pr1_edc_latch0_in	gpio0_12		
	22	UART5_RXD	D16	I	UART5 Receive Data	gmii1_col	rmii2_refclk	spi1_sclk	uart5_rxd	mcasp1_axr2	mmc2_dat3	mcasp0_axr2	gpio3_0		gpio0_0
	23	UART5_RTSN	B13	O	UART5 Request to Send	gmii1_rxer	rmii1_rxer	spi1_d1	I2C1_SCL	mcasp1_fsx	uart5_rtsn	uart2_txd	gpio3_2		
	24	UART5_TXD	A16	O	UART5 Transmit Data	rmii1_refclk	xdma_event_intr2	spi1_cs0	uart5_txd	mcasp1_axr3	mmc0_pow	mcasp1_ahclkx	gpio0_29		
	25	UART5_CTSN	B14	I	UART5 Clear to Send	gmii1_crs	rmii1_crs_dv	spi1_d0	I2C1_SDA	mcasp1_aclkx	uart5_ctsn	uart2_rxd	gpio3_1		
	26	I2C1_SDA	E25	IOD	I2C1 Data	I2C1_SDA					pr1_mii0_rxlink		gpio5_12		
	27	I2C1_SCL	G20	IOD	I2C1 Clock	I2C1_SCL					pr1_mii0_crs		gpio5_10		
	28	GPIO5_8	D25	IO	GPIO5_8						pr1_mii0_col		gpio5_8		
	29	GND													
	30	GND													
	31	DSS_PCLK	A22	O	DSS Pixel Clock	dss_pclk	gpmc_a10	gpmc_a3	pr1_edio_data_in4	pr1_edio_data_out4	pr0_pru1_gpo8	pr0_pru1_gpi8	gpio2_24		
	32	DSS_HSYNC	A23	O	DSS Horizontal Sync	dss_hsync	gpmc_a9	gpmc_a2	pr1_edio_data_in3	pr1_edio_data_out3	pr0_pru1_gpo7	pr0_pru1_gpi7	gpio2_23		
	33	DSS_BIAS	A24	O	DSS Data Enable	dss_ac_bias_en	gpmc_a11	gpmc_a4	pr1_edio_data_in5	pr1_edio_data_out5	pr0_pru1_gpo9	pr0_pru1_gpi9	gpio2_25		
	34	DSS_VSYNC	B23	O	DSS Vertical Sync	dss_vsync	gpmc_a8	gpmc_a1	pr1_edio_data_in2	pr1_edio_data_out2	pr0_pru1_gpo6	pr0_pru1_gpi6	gpio2_22		
	35	DSS_DATA0	B22	IO	DSS Data0	dss_data0	gpmc_a0	pr1_mii_mt0_clk	ehrpwm2A		pr1_pru0_gpo0	pr1_pru0_gpi0	gpio2_6		
	36	DSS_DATA1	A21	IO	DSS Data1	dss_data1	gpmc_a1	pr1_mii0_txen	ehrpwm2B		pr1_pru0_gpo1	pr1_pru0_gpi1	gpio2_7		
	37	DSS_DATA2	B21	IO	DSS Data2	dss_data2	gpmc_a2	pr1_mii0_txd3	ehrpwm2_tripzone_input		pr1_pru0_gpo2	pr1_pru0_gpi2	gpio2_8		
	38	DSS_DATA3	C21	IO	DSS Data3	dss_data3	gpmc_a3	pr1_mii0_txd2	ehrpwm0_synco		pr1_pru0_gpo3	pr1_pru0_gpi3	gpio2_9		
	39	DSS_DATA4	A20	IO	DSS Data4	dss_data4	gpmc_a4	pr1_mii0_txd1	eQEP2A_in		pr1_pru0_gpo4	pr1_pru0_gpi4	gpio2_10		
	40	DSS_DATA5	B20	IO	DSS Data5	dss_data5	gpmc_a5	pr1_mii0_txd0	eQEP2B_in		pr1_pru0_gpo5	pr1_pru0_gpi5	gpio2_11		
	41	DSS_DATA6	C20	IO	DSS Data6	dss_data6	gpmc_a6	pr1_edio_data_in6	eQEP2_index	pr1_edio_data_out6	pr1_pru0_gpo6	pr1_pru0_gpi6	gpio2_12		
	42	DSS_DATA7	E19	IO	DSS Data7	dss_data7	gpmc_a7	pr1_edio_data_in7	eQEP2_strobe	pr1_edio_data_out7	pr1_pru0_gpo7	pr1_pru0_gpi7	gpio2_13		
	43	DSS_DATA8	A19	IO	DSS Data8	dss_data8	gpmc_a12	ehrpwm1_tripzone_input	mcasp0_aclkx	uart5_txd	pr1_mii0_rxd3	uart2_ctsn	gpio2_14		
	44	DSS_DATA9	B19	IO	DSS Data9	dss_data9	gpmc_a13	ehrpwm0_synco	mcasp0_fsx	uart5_rxd	pr1_mii0_rxd2	uart2_rtsn	gpio2_15		
	45	DSS_DATA10	A18	IO	DSS Data10	dss_data10	gpmc_a14	ehrpwm1A	mcasp0_axr0		pr1_mii0_rxd1	uart3_ctsn	gpio2_16		
	46	DSS_DATA11	B18	IO	DSS Data11	dss_data11	gpmc_a15	ehrpwm1B	mcasp0_ahclkr	mcasp0_axr2	pr1_mii0_rxd0	uart3_rtsn	gpio2_17	spi3_cs1	
	47	DSS_DATA12	C19	IO	DSS Data12	dss_data12	gpmc_a16	eQEP1A_in	mcasp0_aclkr	mcasp0_axr2	pr1_mii0_rxlink	uart4_ctsn	gpio0_8	spi3_sclk	
	48	DSS_DATA13	D19	IO	DSS Data13	dss_data13	gpmc_a17	eQEP1B_in	mcasp0_fsr	mcasp0_axr3	pr1_mii0_rxer	uart4_rtsn	gpio0_9	spi3_d0	
	49	DSS_DATA14	C17	IO	DSS Data14	dss_data14	gpmc_a18	eQEP1_index	mcasp0_axr1	uart5_rxd	pr1_mii_mr0_clk	uart5_ctsn	gpio0_10	spi3_d1	
J11A	50	DSS_DATA15	D17	IO	DSS Data15	dss_data15	gpmc_a19	eQEP1_strobe	mcasp0_ahclkx	mcasp0_axr3	pr1_mii0_rxdv	uart5_rtsn	gpio0_11	spi3_cs0	
	51	GND													
	52	GND													
	53	GPIO4_7	AC24	I	Camera1 Data9	cam1_data9		dss_data16	pr0_pru0_gpo17	spi2_cs3	pr0_pru0_gpi17	EMU9	gpio4_7	uart0_ctsn	
	54	GPIO4_6	AA19	I	Camera0 Data9	cam0_data9		dss_data17	pr0_pru0_gpo16	spi2_cs3	pr0_pru0_gpi16	EMU8	gpio4_6		
	55	GPIO4_5	AB19	I	Camera0 Data8	cam0_data8		dss_data18	pr0_pru0_gpo15	spi2_cs2	pr0_pru0_gpi15	EMU7	gpio4_5	I2C2_SCL	
	56	GPIO4_4	AC20	I	CCD Data Pixel Clock	cam0_pclk		dss_data19	pr0_pru0_gpo14	spi2_cs0	pr0_pru0_gpi14	EMU6	gpio4_4	I2C2_SDA	
	57	GPIO4_3	AD17	I	CCD Data Write Enable	cam0_wen		dss_data20	cam0_data11	spi2_d0	cam1_data11	EMU5	gpio4_3		
	58	GPIO4_2	AC18	IO	CCD Data Field Indicator	cam0_field		dss_data21	cam0_data10	spi2_sclk	cam1_data10	EMU4	gpio4_2		
	59	GPIO4_1	AD18	IO	CCD Data Vertical Detect	cam0_vd		dss_data22	pr1_edio_outvalid	spi2_d1	EMU11	EMU3	gpio4_1		
	60	GPIO4_0	AE17	IO	CCD Data Horizontal Detect	cam0_hd		dss_data23	pr1_edio_sof	spi2_cs1	EMU10	EMU2	gpio4_0		
	61	CAM0_DATA0	AE18	I	Camera0 Data0	cam0_data0		cam1_data9	I2C1_SDA	pr0_pru1_gpo16	pr0_pru1_gpi16	ehrpwm0_synco	gpio5_19		
	62	CAM0_DATA1	AB18	I	Camera0 Data1	cam0_data1		cam1_data8	I2C1_SCL	pr0_pru1_gpo17	pr0_pru1_gpi17	ehrpwm3_synco	gpio5_20		
	63	CAM0_DATA2	Y18	I	Camera0 Data2	cam0_data2	mmc1_clk	cam1_data10	qspi_clk				gpio4_24		
	64	CAM0_DATA3	AA18	I	Camera0 Data3	cam0_data3	mmc1_cmd	cam1_data11	qspi_csn				gpio4_25		
	65	CAM0_DATA4	AE19	I	Camera0 Data4	cam0_data4	mmc1_dat0	cam1_wen	qspi_d0			ehrpwm3A	gpio4_26		
	66	CAM0_DATA5	AD19	I	Camera0 Data5	cam0_data5	mmc1_dat1		qspi_d1			ehrpwm3B	gpio4_27		
	67	CAM0_DATA6	AE20	I	Camera0 Data6	cam0_data6	mmc1_dat2		qspi_d2			ehrpwm1A	gpio4_28		
	68	CAM0_DATA7	AD20	I	Camera0 Data7	cam0_data7	mmc1_dat3		qspi_d3			ehrpwm1B	gpio4_29		
	69	MDIO_CLK	B17	O	MDIO Clock	mdio_clk	timer5	uart5_txd	uart3_rtsn	mmc0_sdwp	mmc1_clk	mmc2_clk	gpio0_1	pr1_mdio_mdclk	
	70	GND													
	71	GPIO5_13	E24	IO	SW4 Gpio						pr1_mii1_rxlink		gpio5_13		
	72	MDIO_DATA	A17	IO	MDIO Data	mdio_data	timer6	uart5_rxd	uart3_ctsn	mmc0_sdcd	mmc1_cmd	mmc2_cmd	gpio0_0	pr1_mdio_data	
	73	GPIO1_28	A3	IO	MMC/SD/SDIO Data line 3	gpmc_be1n	gmii2_col	gpmc_csn6	mmc2_dat3	gpmc_dir	pr1_mii1_col	mcasp0_aclkr	gpio1_28		
	74	UART4_RXD	A2	I	UART4 Receive Data	gpmc_wait0	gmii2_crs	gpmc_csn4	rmii2_crs_dv	mmc1_sdcd	pr1_mii1_crs	uart4_rxd	gpio0_30		gpio5_30
	75	RGMII2_TCTL	C3	O	RGMII2 Transmit Control	gpmc_a0	gmii2_txen	rgmii2_tctl	rmii2_txen	gpmc_a16	pr1_mii1_txen	ehrpwm1_tripzone_input	gpio1_16		
	76	UART4_TXD	B3	O	UART4 Transmit Data	gpmc_wpn	gmii2_rxer	gpmc_csn5	rmii2_rxer	mmc2_sdcd	pr1_mii1_rxer	uart4_txd	gpio0_31		gpio5_31
	77	RGMII2_TCLK	E8	O	RGMII2 Transmit Clock	gpmc_a6	gmii2_txclk	rgmii2_tclk	mmc2_dat4	gpmc_a22	pr1_mii_mt1_clk	eQEP1_index	gpio1_22		
	78	RGMII2_RCTL	C5	IO	MMC/SD/SDIO Data line 0	gpmc_a1	gmii2_rxdv	rgmii2_rctl	mmc2_dat0	gpmc_a17	pr1_mii1_rxdv	ehrpwm0_synco	gpio1_17		
	79	RGMII2_TD0	E7	O	RMII2 Transmit Data bit 0	gpmc_a5	gmii2_txd0	rgmii2_td0	rmii2_txd0	gpmc_a21	pr1_mii1_txd0	eQEP1B_in	gpio1_21		
	80	RGMII2_RCLK	F6	O	RGMII2 Receive Clock	gpmc_a7	gmii2_rxclk	rgmii2_rclk	mmc2_dat5	gpmc_a23	pr1_mii_mr1_clk	eQEP1_strobe	gpio1_23		
	81	RGMII2_TD1	D7	O	RMII2 Transmit Data bit 1	gpmc_a4	gmii2_txd1	rgmii2_td1	rmii2_txd1	gpmc_a20	pr1_mii1_txd1	eQEP1A_in	gpio1_20		
	82	RGMII2_RD0	D8	I	RGMII2 Receive Data bit 0	gpmc_a11	gmii2_rxd0	rgmii2_rd0	rmii2_rxd0	gpmc_a27	pr1_mii1_rxd0	mcasp0_axr1	gpio1_27		
	83	RGMII2_TD2	A4	IO	RMII2 Transmit Data bit 2	gpmc_a3	gmii2_txd2	rgmii2_td2	mmc2_dat2	gpmc_a19	pr1_mii1_txd2	ehrpwm1B	gpio1_19		
	84	RGMII2_RD1	G8	I	RGMII2 Receive Data bit 1	gpmc_a10	gmii2_rxd1	rgmii2_rd1	rmii2_rxd1	gpmc_a26	pr1_mii1_rxd1	mcasp0_axr0	gpio1_26		
	85	RGMII2_TD3	C6	IO	RMII2 Transmit Data bit 3	gpmc_a2	gmii2_txd3	rgmii2_td3	mmc2_dat1	gpmc_a18	pr1_mii1_txd3	ehrpwm1A	gpio1_18		
	86	RGMII2_RD2	B4	I	RGMII2 Receive Data bit 2	gpmc_a9	gmii2_rxd2	rgmii2_rd2	mmc2_dat7	gpmc_a25	pr1_mii1_rxd2	mcasp0_fsx	gpio1_25	rmii2_crs_dv	
	87	CLKOUT1	D24	O	Clock out 1	xdma_event_intr0	ext_hw_trigger	timer4	clkout1	spi1_cs1	pr1_pru0_gpi16	EMU2	gpio0_19	pr1_mdio_data	gpio5_28
	88	RGMII2_RD3	F7	I	RGMII2 Receive Data bit 3	gpmc_a8	gmii2_rxd3	rgmii2_rd3	mmc2_dat6	gpmc_a24	pr1_mii1_rxd3	mcasp0_aclkx	gpio1_24		
	89	GPIO2_1	A12	IO	MMC/SD/SDIO Clock	gpmc_clk		gpmc_wait1	mmc2_clk	pr1_mii1_crs	pr1_mdio_mdclk	mcasp0_fsr	gpio2_1		gpio0_4
	90	CLKOUT2	C24	O	Clock out 2	xdma_event_intr1	spi0_cs2	tclkin	clkout2	timer7	pr1_pru0_gpi16	EMU3	gpio0_20	pr1_mdio_mdclk	gpio5_29
	91	GND													
	92	GND													
	93	MMC0_DAT0	C1	IO	MMC/SD/SDIO Data Bus 0	mmc0_dat0	gpmc_a23	uart5_rtsn	uart3_txd	uart1_rin	pr0_pru0_gpo11	pr0_pru0_gpi11	gpio2_29		
	94	MMC0_CLK	D1	IO	MMC/SD/SDIO Clock	mmc0_clk	gpmc_a24	uart3_ctsn	uart2_rxd	dcan1_tx	pr0_pru0_gpo12	pr0_pru0_gpi12	gpio2_30		
	95	MMC0_DAT1	C2	IO	MMC/SD/SDIO Data Bus 1	mmc0_dat1	gpmc_a22	uart5_ctsn	uart3_rxd	uart1_dtrn	pr0_pru0_gpo10	pr0_pru0_gpi10	gpio2_28		
	96	MMC0_CMD	D2	IO	MMC/SD/SDIO Command	mmc0_cmd	gpmc_a25	uart3_rtsn	uart2_txd	dcan1_rx	pr0_pru0_gpo13	pr0_pru0_gpi13	gpio2_31		
	97	MMC0_DAT2	B2	IO	MMC/SD/SDIO Data Bus 2	mmc0_dat2	gpmc_a21	uart4_rtsn	timer6	uart1_dsrn	pr0_pru0_gpo9	pr0_pru0_gpi9	gpio2_27		
	98	MMC0_SDCD	R25	IO	SD Card Detect	spi0_cs1	uart3_rxd	eCAP1_in_PWM1_out	mmc0_pow	xdma_event_intr2	mmc0_sdcd	EMU4	gpio0_6	ehrpwm2A	timer0
	99	MMC0_DAT3	B1	IO	MMC/SD/SDIO Data Bus 3	mmc0_dat3	gpmc_a20	uart4_ctsn	timer5	uart1_dcdn	pr0_pru0_gpo8	pr0_pru0_gpi8	gpio2_26		
	100	GND													
	101	VIN_5V													
	102	VIN_5V													
	103	VIN_5V													
	104	VIN_5V													
	105	GND													
	106	GND													
	107	V3_3D													
	108	GND													
	109	V3_3D													
	110	V1_8D													
	111	GND													

	112	VRTC												
	113	PMIC_PB		I	Push-button Monitor Input									
	114	GND												
	115	RESET_IN		I	System Manual Reset									
	116	SYS_RESETn		O	System Reset									
	117	LCD_BLEN	G24	IO	LCD Backlight Enable	eCAP0_in_PWM0_out	uart3_txd	spi1_cs1	pr1_ecap0_ecap_capin_apwm_o	spi1_sclk	mmc0_sdwp	xdma_event_intr2	gpio0_7	ehrpwm2B	timer1
	118	GPIO0_24	H20	IO	Use For DSS Reset	clkreq							gpio0_24		
	119	MCASP0_AHCLKR	M24	IO	McASP0 Receive Master Clock	mcasp0_ahclkr	ehrpwm0_synci	mcasp0_axr2	spi1_cs0	eCAP2_in_PWM2_out	pr0_pru0_gpo3	pr0_pru0_gpi3	gpio3_17		
	120	MCASP1_AXR1	L24	IO	McASP0 Transmit Master Clock	mcasp0_ahclkx	eQEP0_strobe	mcasp0_axr3	mcasp1_axr1	EMU4	pr0_pru0_gpo7	pr0_pru0_gpi7	gpio3_21		
	121	MCASP0_AXR0	H23	IO	McASP0 Serial Data (IN/OUT)	mcasp0_axr0	ehrpwm0_tripzone_input	spi1_cs3	spi1_d1	mmc2_sdcd	pr0_pru0_gpo2	pr0_pru0_gpi2	gpio3_16		
	122	MCASP1_AXR0	M25	IO	McASP0 Serial Data (IN/OUT)	mcasp0_axr1	eQEP0_index	mcasp1_axr0	EMU3	pr0_pru0_gpo6	pr0_pru0_gpi6	gpio3_20	gpio0_2		
	123	MCASP0_FSX	N22	IO	McASP0 Transmit Frame Sync	mcasp0_fsx	ehrpwm0B	spi1_cs2	spi1_d0	mmc1_sdcd	pr0_pru0_gpo1	pr0_pru0_gpi1	gpio3_15		
	124	MCASP1_FSX	K23	IO	McASP0 Receive Frame Sync	mcasp0_fsr	eQEP0B_in	mcasp0_axr3	mcasp1_fsx	EMU2	pr0_pru0_gpo5	pr0_pru0_gpi5	gpio3_19		gpio0_19
	125	MCASP0_ACLKX	N24	IO	McASP0 Transmit Bit Clock	mcasp0_aclkx	ehrpwm0A	spi0_cs3	spi1_sclk	mmc0_sdcd	pr0_pru0_gpo0	pr0_pru0_gpi0	gpio3_14		
	126	MCASP1_ACLKX	L23	IO	McASP0 Receive Bit Clock	mcasp0_aclkr	eQEP0A_in	mcasp0_axr2	mcasp1_aclkx	mmc0_sdwp	pr0_pru0_gpo4	pr0_pru0_gpi4	gpio3_18		gpio0_18
	127	UART3_RTSn	K24	O	UART3 Request to Send	uart3_rtsn	hdq_sio			pr0_pru1_gpo19	pr0_pru1_gpi19	ehrpwm5B	gpio5_1		
	128	UART3_RXD	H25	IO	UART3 Receive Data	uart3_rxd				pr0_pru0_gpo18	pr0_pru0_gpi18	ehrpwm4A	gpio5_2		
	129	MCASP0_AHCLKR	M24	IO	McASP0 Receive Master Clock	mcasp0_ahclkr	ehrpwm0_synci	mcasp0_axr2	spi1_cs0	eCAP2_in_PWM2_out	pr0_pru0_gpo3	pr0_pru0_gpi3	gpio3_17		
	130	MCASP1_AXR1	L24	IO	McASP0 Transmit Master Clock	mcasp0_ahclkx	eQEP0_strobe	mcasp0_axr3	mcasp1_axr1	EMU4	pr0_pru0_gpo7	pr0_pru0_gpi7	gpio3_21		gpio0_3
	131	GND													
	132	GND													
	133	SPI2_CS0	T23	IO	Use For LCD Reset	spi2_cs0	I2C1_SDA					ehrpwm2_tripzone_input	gpio3_25		gpio0_23
	134	SPI2_SCLK	N20	IO	Use For HDMI_INTn	spi2_sclk	I2C1_SCL					ehrpwm4_tripzone_input	gpio3_24		gpio0_22
	135	SPI2_D0	P22	IO	Use For TOUCH_INTn	spi2_d0						ehrpwm5_tripzone_input	gpio3_22		gpio0_20
	136	SPI2_D1	P20	IO	Use For TP Reset	spi2_d1						ehrpwm1_tripzone_input	gpio3_23		gpio0_21
	137	SPI0_CS0	T20	IO	SPI0 Chip Select0	spi0_cs0	mmc2_sdwp	I2C1_SCL	ehrpwm0_synci	pr1_uart0_txd	pr0_uart0_txd	pr1_edio_data_out1	gpio0_5	ehrpwm1B	
	138	SPI0_SCLK	P23	IO	SPI0 Clock	spi0_sclk	uart2_rxd	I2C2_SDA	ehrpwm0A	pr1_uart0_cts_n	pr0_uart0_cts_n	EMU2	gpio0_2		
	139	SPI0_D0	T22	IO	SPI0 Data line 0	spi0_d0	uart2_txd	I2C2_SCL	ehrpwm0B	pr1_uart0_rts_n	pr0_uart0_rts_n	EMU3	gpio0_3		
	140	SPI0_D1	T21	IO	SPI0 Data line 1	spi0_d1	mmc1_sdwp	I2C1_SDA	ehrpwm0_tripzone_input	pr1_uart0_rxd	pr0_uart0_rxd	pr1_edio_data_out0	gpio0_4	ehrpwm1A	
	141	USB1_DRVVBUS	F25	O	USB1 Active high VBUS control	USB1_DRVVBUS							gpio3_13		gpio0_25
	142	GND													
	143	USB0_DRVVBUS	G21	O	USB0 Active high VBUS control	USB0_DRVVBUS							gpio0_18		gpio5_27
	144	USB1_DP	V24	A	USB1 Data plus	USB1_DP									
	145	USB1_VBUS	T25	A	USB1 VBUS	USB1_VBUS									
	146	USB1_DM	V25	A	USB1 Data minus	USB1_DM									
	147	USB0_VBUS	U23	A	USB0 VBUS	USB0_VBUS									
	148	GND													
	149	USB1_ID	U25	A	USB1 ID	USB1_ID									
J11B	150	USB0_DP	W25	A	USB0 Data plus	USB0_DP									
	151	USB0_ID	U24	A	USB0 ID	USB0_ID									
	152	USB0_DM	W24	A	USB0 Data minus	USB0_DM									
	153	SPI4_D1	P24	IO	SPI4 Data line 1	spi4_d1						ehrpwm0_tripzone_input	gpio5_6		
	154	GND													
	155	SPI4_D0	R24	IO	SPI4 Data line 0	spi4_d0						ehrpwm3_synci	gpio5_5		
	156	I2C0_SCL	Y22	IOD		I2C0_SCL	timer7	uart2_rtsn	eCAP1_in_PWM1_out				gpio3_6		
	157	SPI4_SCLK	P25	IO	SPI4 Clock	spi4_sclk						ehrpwm0_synci	gpio5_4		
	158	I2C0_SDA	AB24	IOD		I2C0_SDA	timer4	uart2_ctsn	eCAP2_in_PWM2_out				gpio3_5		
	159	SPI4_CS0	N25	IO	SPI4 Chip Select 0	spi4_cs0						ehrpwm3_tripzone_input	gpio5_7		
	160	CAM1_FIELD	AC25	IO	CCD Data Field Indicator	cam1_field	xdma_event_intr7	ext_hw_trigger	cam0_data10	spi2_cs1	cam1_data10	ehrpwm1B	gpio4_12	ehrpwm3A	
	161	CAM1_WEN	AB25	I	CCD Data Write Enable	cam1_wen	xdma_event_intr8	pr1_edio_sof	cam0_data11	spi2_d1	cam1_data11	EMU11	gpio4_13	ehrpwm3B	
	162	CAM1_HSYNC	AD25	IO	CCD Data Horizontal Detect	cam1_hd	xdma_event_intr4	spi0_cs3	pr0_pru1_gpo1	spi2_cs0	pr0_pru1_gpi1	ehrpwm0A	gpio4_9		
	163	CAM1_VSYNC	AC23	IO	CCD Data Vertical Detect	cam1_vd	xdma_event_intr5	spi1_cs2	pr0_pru1_gpo2	spi2_cs2	pr0_pru1_gpi2	ehrpwm0B	gpio4_10		
	164	CAM1_PCLK	AE21	I	CCD Data Pixel Clock	cam1_pclk	xdma_event_intr6	spi1_cs3	pr0_pru1_gpo3	spi2_sclk	pr0_pru1_gpi3	ehrpwm1A	gpio4_11		
	165	GND													
	166	GND													
	167	CAM1_DATA0	AB20	I	Camera1 Data0	cam1_data0	uart1_rxd	spi3_d0	I2C2_SDA			ehrpwm0_tripzone_input	gpio4_14		
	168	CAM1_DATA1	AC21	I	Camera1 Data1	cam1_data1	uart1_txd	spi3_d1	I2C2_SCL			ehrpwm0_synci	gpio4_15		
	169	CAM1_DATA2	AD21	I	Camera1 Data2	cam1_data2	uart1_ctsn	spi3_cs0	mmc2_clk	pr0_pru1_gpo10	pr0_pru1_gpi10	ehrpwm1_tripzone_input	gpio4_16		
	170	CAM1_DATA3	AE22	I	Camera1 Data3	cam1_data3	uart1_rtsn	spi3_sclk	mmc2_cmd	pr0_pru1_gpo11	pr0_pru1_gpi11	pr1_edc_latch0_in	gpio4_17		
	171	CAM1_DATA4	AD22	I	Camera1 Data4	cam1_data4	uart1_rin	uart2_rxd	mmc2_dat0	pr0_pru1_gpo12	pr0_pru1_gpi12	pr1_edc_latch1_in	gpio4_18	uart0_dcdn	
	172	CAM1_DATA5	AE23	I	Camera1 Data5	cam1_data5	uart1_dsrn	uart2_txd	mmc2_dat1	pr0_pru1_gpo13	pr0_pru1_gpi13	pr1_edio_latch_in	gpio4_19		
	173	CAM1_DATA6	AD23	I	Camera1 Data6	cam1_data6	uart1_dcdn	uart2_ctsn	mmc2_dat2	pr0_pru1_gpo14	pr0_pru1_gpi14	pr1_edio_data_in0	gpio4_20		
	174	CAM1_DATA7	AE24	I	Camera1 Data7	cam1_data7	uart1_dtrn	uart2_rtsn	mmc2_dat3	pr0_pru1_gpo15	pr0_pru1_gpi15	pr1_edio_data_in1	gpio4_21		
	175	CAM1_DATA8	AD24	I	Camera1 Data8	cam1_data8	xdma_event_intr3	spi0_cs2	pr0_pru1_gpo0	spi2_d0	pr0_pru1_gpi0	EMU10	gpio4_8	uart0_rtsn	
	176	JTAG_TCK	AA25	I	JTAG TEST CLOCK	TCK									
	177	JTAG_TDO	AA24	O	JTAG TEST DATA OUTPUT	TDO									
	178	JTAG_TMS	Y24		JTAG TEST MODE SELECT	TMS									
	179	JTAG_TDI	Y20		JTAG TEST DATA INPUT	TDI									
	180	JTAG_TRSTn	Y25		JTAG TEST RESET (ACTIVE LOW)	nTRST									
	181	GNDA_ADC													
	182	GNDA_ADC													
	183	ADC1_AIN0	AC16	A	ADC1 Analog Input/Output 0	ADC1_AIN0									
	184	ADC1_AIN1	AB16	A	ADC1 Analog Input/Output 1	ADC1_AIN1									
	185	ADC1_AIN2	AA16	A	ADC1 Analog Input/Output 2	ADC1_AIN2									
	186	ADC1_AIN3	AB15	A	ADC1 Analog Input/Output 3	ADC1_AIN3									
	187	ADC1_AIN4	AA15	A	ADC1 Analog Input/Output 4	ADC1_AIN4									
	188	ADC1_AIN5	Y15	A	ADC1 Analog Input/Output 5	ADC1_AIN5									
	189	ADC1_AIN6	AE16	A	ADC1 Analog Input/Output 6	ADC1_AIN6									
	190	ADC1_AIN7	AD16	A	ADC1 Analog Input/Output 7	ADC1_AIN7									
	191	ADC0_AIN0	AA12	A	ADC0 Analog Input/Output 0	ADC0_AIN0									
	192	ADC0_AIN1	Y12	A	ADC0 Analog Input/Output 1	ADC0_AIN1									
	193	ADC0_AIN2	Y13	A	ADC0 Analog Input/Output 2	ADC0_AIN2									
	194	ADC0_AIN3	AA13	A	ADC0 Analog Input/Output 3	ADC0_AIN3									
	195	ADC0_AIN4	AB13	A	ADC0 Analog Input/Output 4	ADC0_AIN4									
	196	ADC0_AIN5	AC13	A	ADC0 Analog Input/Output 5	ADC0_AIN5									
	197	GNDA_ADC													
	198	GNDA_ADC													
	199	VDDA_ADC0													
	200	VDDA_ADC1													